



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: R. William Ezell
Serial No.: 10/629,019
Filing Date: July 28, 2003
Group Art Unit: Unknown
Examiner: Unknown
Title: METHOD AND SYSTEM FOR FILTER TUNING USING A
DIGITAL TRIM VALUE

Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Copies of the references are enclosed for the convenience of the Examiner. No representation is made that a search has been made, that the references are material to the patentability of the present application, or that the references qualify as prior art.

REMARKS

This Information Disclosure Statement is being submitted pursuant to 37 C.F.R. § 1.97(b) before the issuance of a first Office Action and, therefore, no fee is believed to be due. If, however, a fee is due, the Commissioner is hereby authorized to charge such fee to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

Baker Botts L.L.P.
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Date: August 12, 2003

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PTO-1449 O I P E Information Disclosure Citation In an Application AUG 12 2003	Application No. 10/629,019		Applicant(s) R. William Ezell	
	Docket Number 073671.0171	Group Art Unit Unknown	Filing Date July 28, 2003	

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	K							
	L							
	M							
	N							

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
O	Geiger, et al., "VLSI Design Techniques for Analog and Digital Circuits," McGraw-Hill Publishing Company, pp. 651-656 plus title and copyright pages	1990
P	Martinez, et al., "A 60-mW 200-MHz Continuous-Time Seventh-Order Linear Phase Filter With On-Chip Automatic Tuning System," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, pp. 216-225 plus cover page	02/2003
Q		
R		
S		
T		

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE